EchoCore

AI compute that endures — without the power bill, the heat, or the technician.

THE VOID IT FILLS

Autonomous systems at the tactical edge — drones, space assets, remote sensors — are limited by three things: power, heat, and the absence of anyone to fix them. Today's processors spend most of their energy just moving data between memory and compute, and they fail quietly in radiation and heat. Missions end when the processor does. No fielded chip today both computes efficiently and maintains itself for years unattended.

WHAT ECHOCORE DOES

EchoCore is a proprietary AI processor in development that **computes in memory and reduces memory-to-processor data movement** — the traffic that consumes most of the energy in today's AI hardware. The result: **more inference per watt**, so smaller batteries run longer missions. It is designed for **radiation tolerance and thermal stability** from the start, rather than retrofitted shielding, and it **monitors its own health and refreshes itself** before degradation becomes mission-critical failure — built for **multi-year unattended deployment**. It runs AI workloads and continuous monitoring at the edge: sensing, inference, and system-health reporting without a round trip to the cloud.

TARGET APPLICATIONS

- **SWaP-constrained unmanned systems** — air, surface, undersea.
- **Space and cislunar edge compute;** resilient PNT.
- **Remote and denied-environment sensors** — high-radiation, no-maintenance monitoring.
- **Energy, nuclear, and harsh-industry monitoring;** agriculture and environmental sensing (dual-use).

INTELLECTUAL PROPERTY

- Proprietary architecture, **U.S.-owned and U.S.-held**.
- Technical details shared **under NDA** with qualified U.S. government and U.S. parties.

EXPERIENCE

- **Operating software portfolio in production:** the company designs, ships, and operates governed AI systems moving real money today (see Castellan capability statement) — the software layer EchoCore is designed to run.
- **Architecture, process-feasibility, and reliability simulations complete;** Phase I feasibility plan with go/no-go criteria defined.
- **Team:** full-stack engineering leadership (CEO) with 14+ years in education and training; founder-led R&D program since 2025.
- **Ecosystem:** Florida APEX Accelerator client · UF Innovate | Accelerate founder community · DEFENSEWERX events (SOFWERX) · NSTXL application submitted.

MADE IN THE USA

- **Designed in Florida with the intent to manufacture in the United States;** domestic fabrication path identified.
- Seeking: SBIR/STTR Phase I (DoD, NASA, NSF) and consortium participation.

COMPANY DATA

UEI	C4KGWJ6T4BB3
CAGE	22QH8
NAICS	541715
SBC ID	SBC_002680767

CERTIFICATIONS & STATUS

SAM.gov Active · Florida LLC
Small Business · Minority-owned
CMMC Level 2 self-assessment in progress (NIST SP 800-171)
Registered: DSIP, Grants.gov, Research.gov, SBA Company Registry

DISCLOSURE

This statement describes what the processor does, not how. Implementation, materials system, and IP position are shared under NDA with qualified parties.

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